



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : **Confirmation No. 5635**
Kazushi HIGASHI et al. : Docket No. 2001_1194A
Serial No. 09/940,876 : Group Art Unit 2827
Filed August 29, 2001 : Examiner D.E. GRAYBILL

METHOD FOR ASSEMBLING INTEGRAL
TYPE ELECTRONIC DEVICE AND INTEGRAL
TYPE ELECTRONIC DEVICE (as amended)

THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
FEES FOR THIS PAPER TO DEPOSIT
ACCOUNT NO. 23-0975

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to the Restriction Requirement of September 29, 2003.

Applicants, by their undersigned representative, hereby elect the invention of Group I which is drawn to a method for assembling an integral electronic device and is represented by claims 11-23. Please note that claims 1-10 have been cancelled. This election is traversed for the following reasons.

The basis for the restriction requirement as set forth by the Examiner, i.e. that the product as claimed can be made by another and material of different process such as a process of connecting the second board to the component while the component is not held in the opening of the first board, existed with regard to original claims 1 and 3, yet the Examiner failed to restrict claim 1 from claim 3. In this regard, method claim 1 recited, "electrically connecting a second board to the electronic component held to the first board", and product claim 3 recited, "a second board which is electrically connected to the electronic component". The phrase "held to the first board" as recited in claim 1 corresponds to the phrase "while...in said opening" (of the first board) as recited in claim 11. Accordingly, because the product of claim 3 could have been restricted from the method of claim 1, but was not, the Examiner has apparently already considered the alternative processes by which the

product of claim 3 could be made, such that there would be no undue burden on the Examiner to continue considering these alternative processes.

If the Examiner finds the above not to be persuasive to withdraw the restriction requirement, then because method claims 11-23 are so closely related to product claims 24-35, which are similar to original method claims 1 and 2 and original product claims 3-10, and because claims 1 and 2 were examined along with claims 3-10, it is respectfully submitted that there would be no undue burden on the Examiner to continue examining method claims 11-23 and product claims 24-35 together.

Irrespective of the above, claim 11 has been amended so as to remove the Examiner's basis for the restriction requirement. In this regard, claim 11 has been amended by deleting the phrase "while said electronic component is held in said opening". Accordingly, it now respectfully submitted that the product as claimed cannot be made by another and material of different process than that claimed, such that claims 11-23 should no longer be restricted from claims 24-35.

Thus, it is respectfully submitted that the Examiner withdraw the restriction requirement and examine Groups I and II, i.e. claims 11-35, together.

Having made the required election, a full examination on the merits of the elected invention is hereby requested.

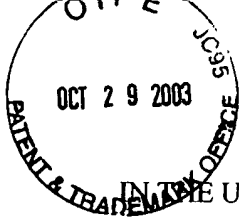


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METHOD FOR ASSEMBLING INTEGRAL TYPE
ELECTRONIC DEVICE AND INTEGRAL TYPE
ELECTRONIC DEVICE (as amended)

PATENT OFFICE FEE TRANSMITTAL FORM

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached hereto is a check in the amount of \$18.00 to cover Patent Office fees relating to filing the following attached papers:

Additional Claims Fee Transmittal Letter
Excess of Twenty \$18.00

A duplicate copy of this paper is being submitted for use in the Accounting Division, Office of Finance.

The Commissioner is authorized to charge any deficiency or to credit any overpayment associated with this communication to Deposit Account No. 23-0975, with the EXCEPTION of deficiencies in fees for multiple dependent claims in new applications.

Respectfully submitted,

Kazushi HIGASHI et al.

By

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October 29, 2003

[Check No. 58441]

2001_1194A



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METHOD FOR ASSEMBLING INTEGRAL
TYPE ELECTRONIC DEVICE AND INTEGRAL
TYPE ELECTRONIC DEVICE (as amended)

ADDITIONAL CLAIMS FEE TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
FEES FOR THIS PAPER TO DEPOSIT
ACCOUNT NO. 23-0975

Sir:

Transmitted herewith is an Amendment in the above-identified application. Additional fees required as a result of this Amendment are calculated as follows:

	SMALL ENTITY		LARGE ENTITY
Total Claims exceeding 20 (not already paid for): 1 x	(\$ 9 = \$)	or	(\$18 = \$18)
Indep. Claims exceeding 3 (not already paid for): x	(\$43 = \$)	or	(\$86 = \$)
<input type="checkbox"/> Multiple Dep. Claim(s) (if there previously were none): +	(\$145 = \$)	or	(\$290 = \$)
Total Additional Fee =	<u>\$</u>	or	<u>\$18</u>

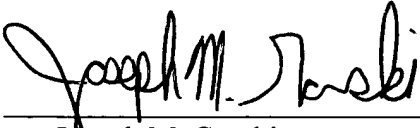
- ☐ Small entity status of this application is established by the verified statement under 37 C.F.R. 1.9 and 1.27 which
- ☐ is enclosed or
- ☐ has been previously submitted.

☒ A check in the amount of \$18.00 is enclosed.

☐ Please charge Deposit Account No. 23-0975 the amount of \$ to cover additional fee. The Commissioner is authorized to charge any deficiency associated with this communication or to credit any overpayment to the Deposit Account. The original and two copies of this document are enclosed.

Respectfully submitted,

Kazushi HIGASHI et al.

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October 29, 2003